

FIG. 1

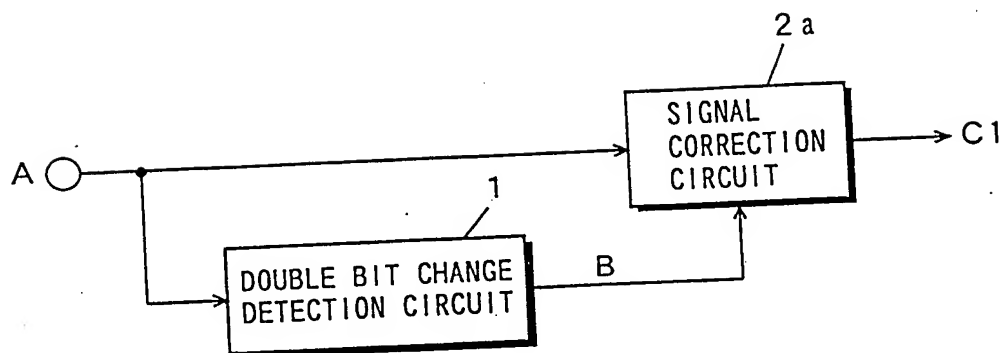


FIG. 2

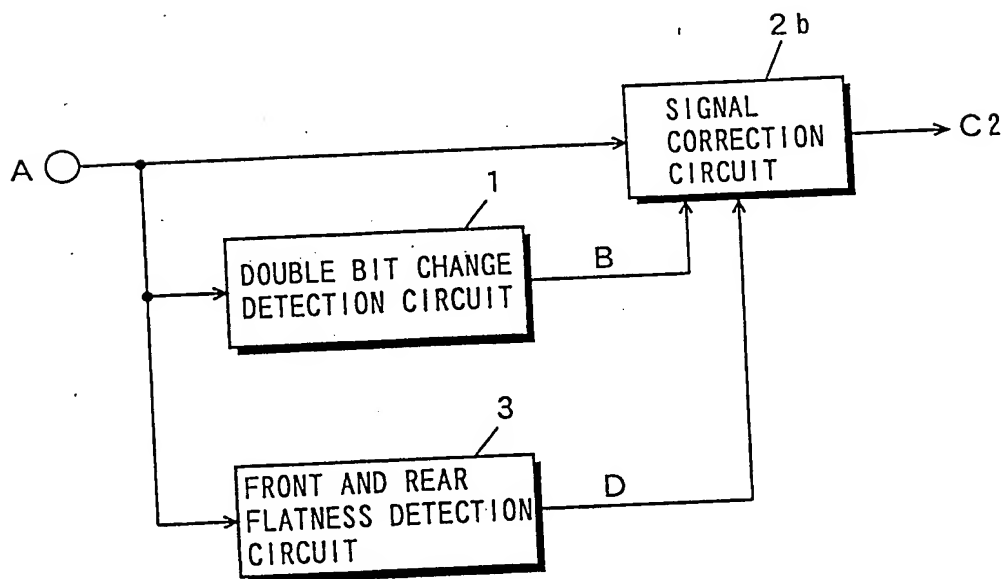


FIG. 3

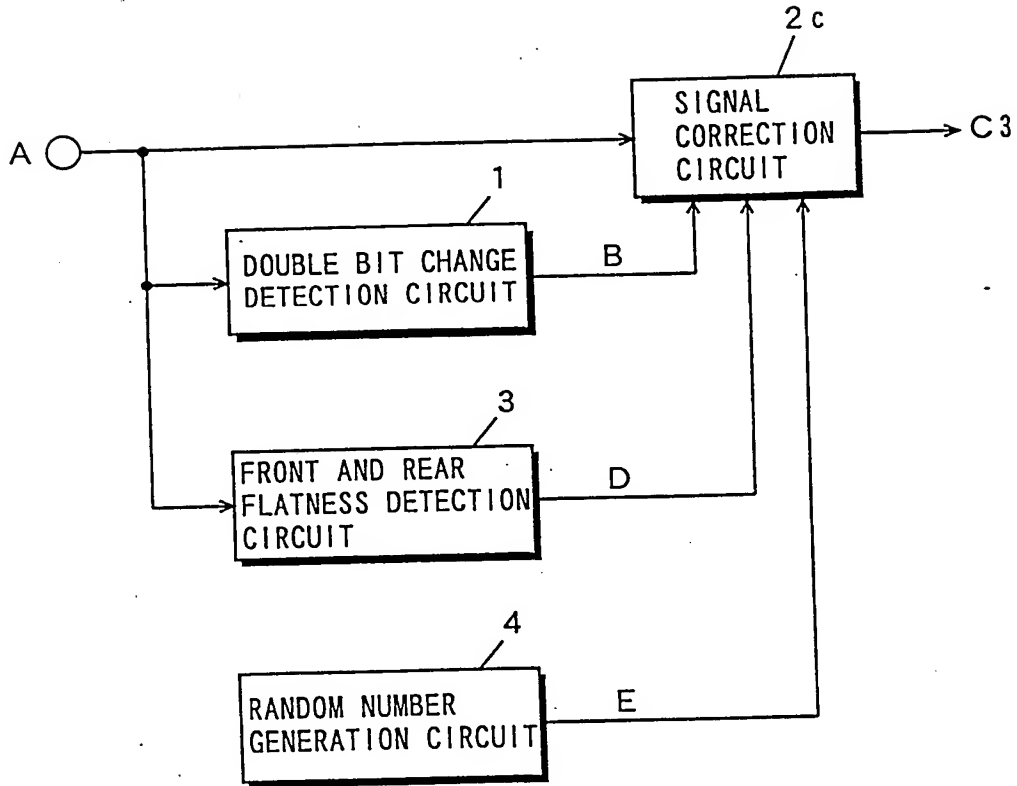
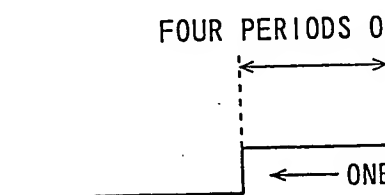


FIG. 4 A  ← ONE-BIT CHANGE

FIG. 4 B  ← DOUBLE BIT CHANGE

FIG. 4 C  ← ONE-BIT CHANGE  
← ONE-BIT CHANGE

FOUR PERIODS OF CLOCK SIGNAL

FIG. 5

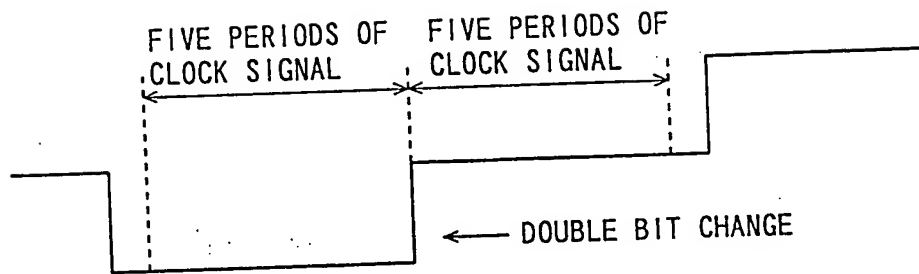
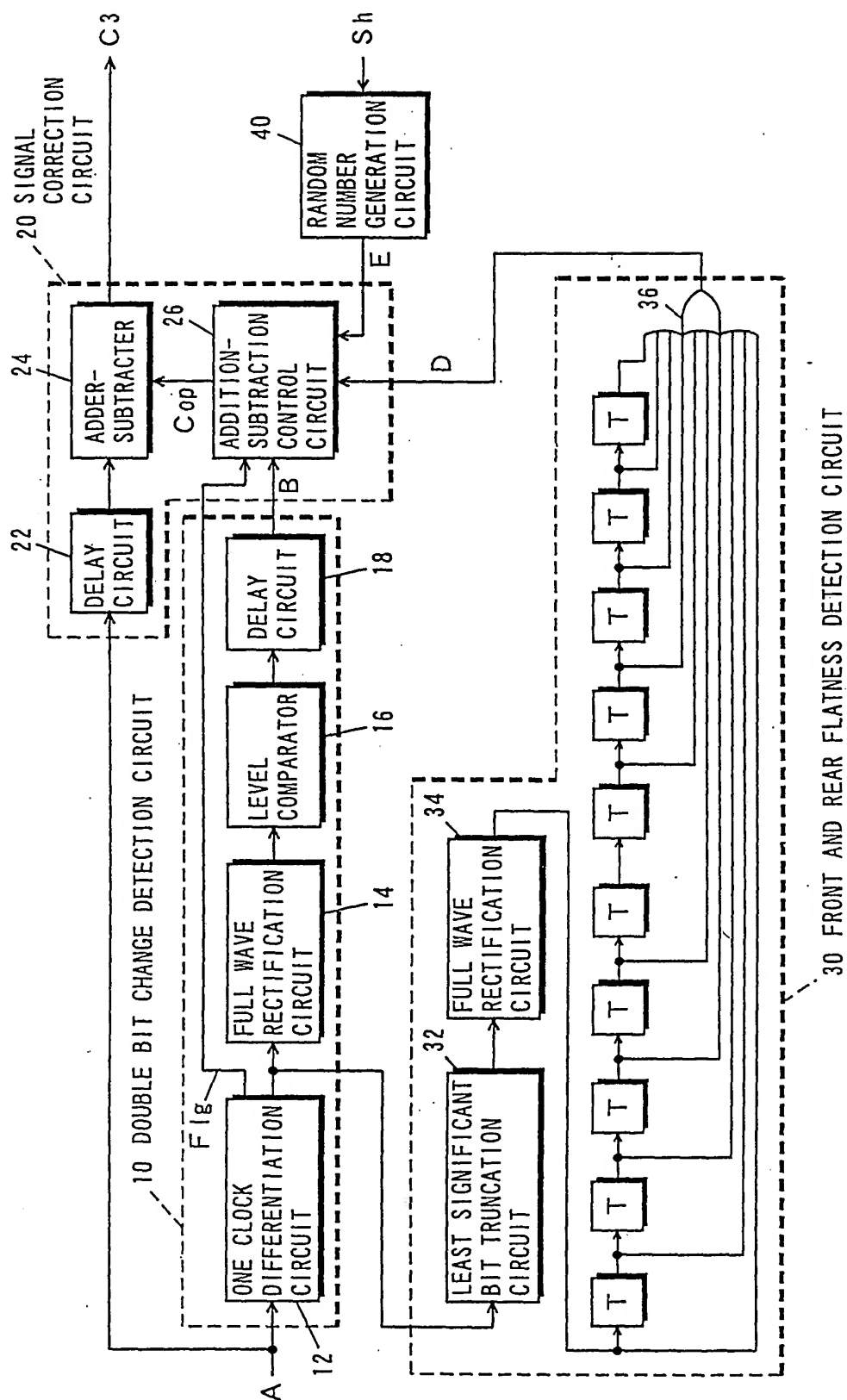


FIG. 6

SCANNING LINE	AFTER FALSE CONTOUR CORRECTION	
1	<p>Diagram (a) shows a vertical dashed line passing through a series of horizontal lines. The lines are arranged in a staircase pattern, with each line shifted to the right relative to the one below it. The dashed line is vertical, indicating a constant horizontal position across all scanning lines.</p>	<p>Diagram (b) shows a zigzag dashed line passing through a series of horizontal lines. The lines are arranged in a staircase pattern, with each line shifted to the right relative to the one below it. The dashed line is zigzag, indicating a varying horizontal position across the scanning lines.</p>
2		
3		
4		
5		
6		
	(a)	(b)

FIG. 7



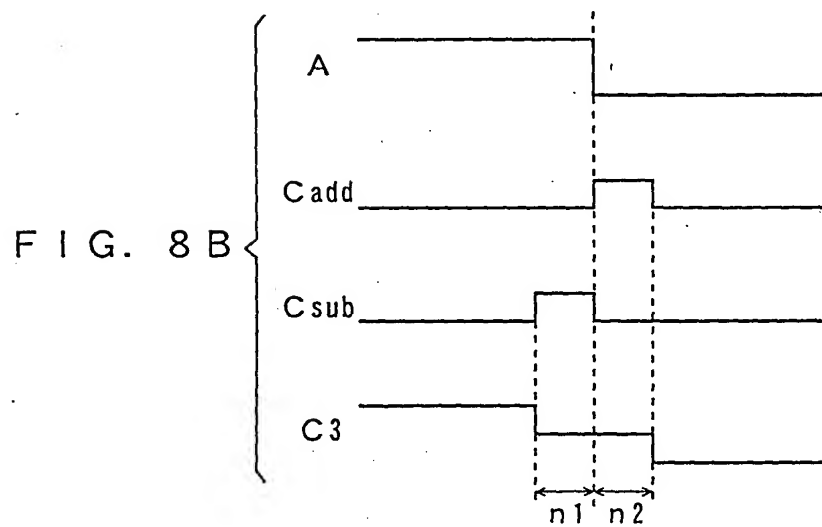
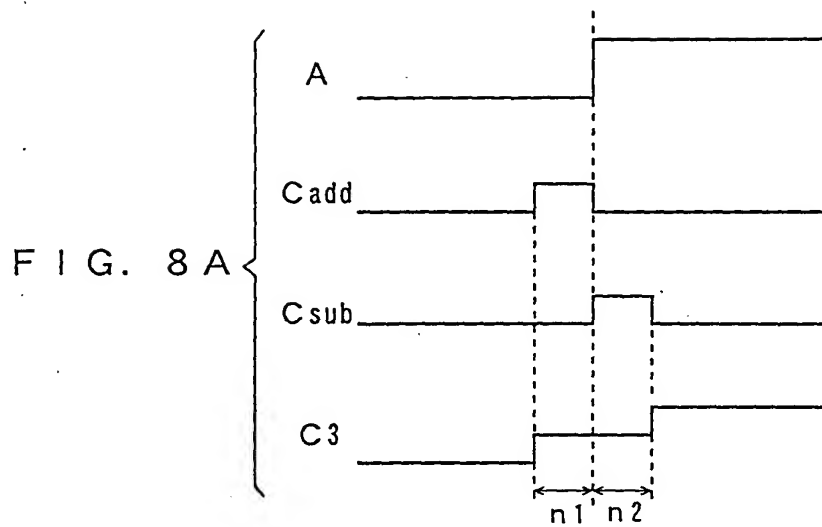


FIG. 9 PRIOR ART

